Electronic Acknowledgement Receipt FES ID: 1227907 Application Number: 10604491 Confirmation Number: 1490 Multiprocessor subsystem in SoC with bridge between processor Title of Invention: clusters interconnection and SoC system bus First Named Inventor: Christos J. Georgiou Customer Number: 23389 Filer: Steven Fischman/Grace Colucci Filer Authorized By: Steven Fischman Attorney Docket Number: FIS920030113 Receipt Date: 29-SEP-2006 Filing Date: 25-JUL-2003 Time Stamp: 15:28:31 Application Type: Utility International Application Number: Payment information: Submitted with Payment yes Payment was successfully received in RAM \$790

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1	Request for Continued Examination (RCE)	FIS920030113US1_RCE.pdf	112787	no	2
Warnings:	60.0				
Information:					
2	Information Disclosure Statement (IDS) Filed	FIS920030113US1_IDS.pdf	3325380	no	5
Warnings:		1			
Information:					
3	NPL Documents	Almasi.pdf	639895	no	17
Warnings:					
Information:					
4	NPL Documents	Georgiou.pdf	512967	no	9
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.